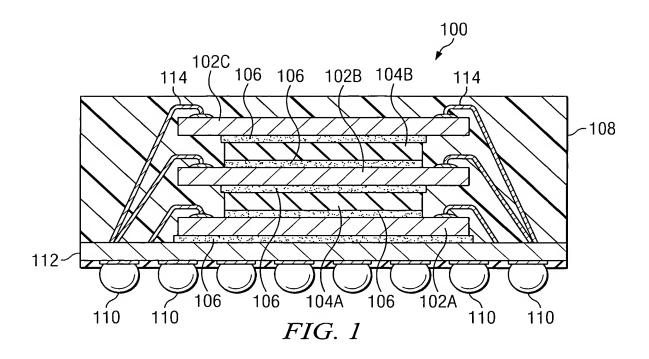
Multiple Die-Spacer
for an Integrated Circuit
Inventor: Edgardo R. Hortaleza
Attorney Docket No. TI- 36656 (032350.B565)
Filed: January 23, 2004
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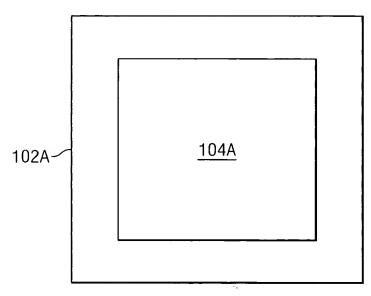
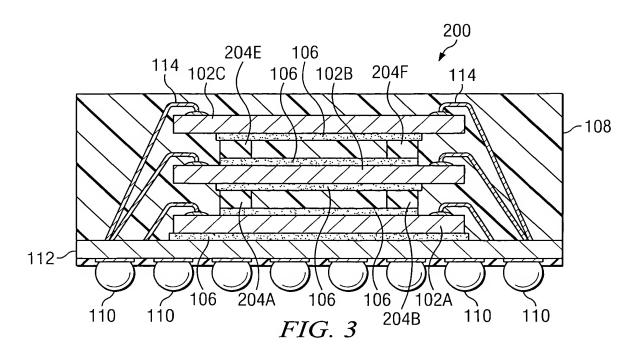


FIG. 2

Multiple Die-Spacer
for an Integrated Circuit
Inventor: Edgardo R. Hortaleza
Attorney Docket No. TI- 36656 (032350.B565)
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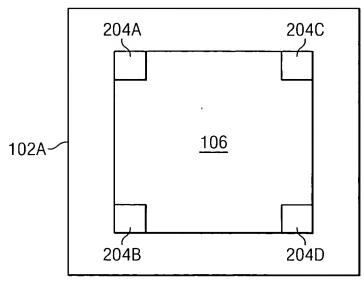


FIG. 4

Multiple Die-Spacer
for an Integrated Circuit
Inventor: Edgardo R. Hortaleza
Attorney Docket No. TI- 36656 (032350.B565)
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